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PATENT
Customer No. 22,852
Attorney Docket No. 4329.2724

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent No.: 6,895,548 B2)
)
Inventors: MASATSUGU KITADA ET AL.)
)
Issue Date.: May 17, 2005)
)
For: SEMICONDUCTOR TESTING)
APPARATUS AND METHOD FOR)
OPTIMIZING A WAIT TIME UNTIL)
STABILIZATION OF)
SEMICONDUCTOR DEVICE)
OUTPUT SIGNAL)

Certificate
JUN 23 2005
of Correction

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

REQUEST FOR CERTIFICATE OF CORRECTION

Pursuant to 35 U.S.C. § 254, and 37 C.F.R. § 1.322, this is a request for a Certificate of Correction in the above-identified patent. The mistakes identified in the appended Form occurred through the fault of the Patent Office, as clearly disclosed by the records of the application which matured into this patent.

Two (2) copies of PTO Form 1050 are appended. The complete Certificate of Correction involves one (1) page. Issuance of the Certificate of Correction containing the correction is earnestly requested.

JUN 24 2005

Please charge any required fees not included herewith to our deposit account
06-0916.


Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,
GARRETT & DUNNER, L.L.P.

Dated: _____

6/17/05

By: _____


Richard V. Burgujian
Reg. No. 31,744

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. 6,895,548 B2

DATED: May 17, 2005

INVENTORS: Kitada et al.

It is hereby certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Claim 1, column 8, line 12, change "comprising" to --comprising--.

Claim 1, column 8, line 37, change "maxima/" to --maximal--.

Claim 6, column 10, line 50, change "d4a" to --data--.

Claim 8, column 10, line 60, change "devices," to --device,--.

MAILING ADDRESS OF SENDER

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Patent No. 6,895,548 B2

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